

Notice of References Cited	Application/Control No. 10/091,530	Applicant(s)/Patent Under Reexamination GEMELLI ET AL.	
	Examiner Gopal C. Ray	Art Unit 2111	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,345,052	02-2002	Tse et al.	370/395.6
	B	US-6,034,545	03-2000	Flynn, David Walter	326/39
	C	US-5,832,255	11-1998	Roberts et al.	713/500
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"A digital CMOS programmable clock generator" by Holler, P.T.; Lee, H. (abstract only) Publication Date:19-23 Sept. 1994
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.